



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sohrab Kianian et al.

Serial No. 09/982,413

Group Art Unit: 2826

Filed: October 17, 2001

Examiner: Scott R. Wilson

Title: **SEMICONDUCTOR MEMORY ARRAY OF FLOATING GATE MEMORY
CELLS WITH BURIED BIT-LINE AND VERTICAL WORD LINE TRANSISTOR**

Certificate of Mailing

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October 8, 2004


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**PETITION UNDER 37 C.F.R. 1.313(c)(2) TO WITHDRAW
APPLICATION FROM ISSUANCE**

MAIL STOP 313(C)
COMMISSIONER FOR PATENTS
P.O. BOX 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby petitions the Director under the provisions of 37 CFR § 1.313(c)(2) for withdrawal of the above patent application from issue after payment of the issue fee for consideration of a Request for Continued Examination (RCE), and an Information Disclosure Statement. The issue fee in this application was timely paid on July 1, 2004.

Applicant is filing herewith a Request for Continued Examination, an Information Disclosure Statement, and PTO Form 1449 listing the references for consideration by the Examiner.

A check in the amount of \$920.00 is enclosed which includes the Petition fee (\$130) and RCE fee (\$790). Please charge any additional fees, or make any credits to Deposit Account No. 07-1896. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Gray Cary Ware & Freidenrich LLP

Dated: October 8, 2004



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